

WEST Search History

DATE: Thursday, June 09, 2005

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L115	l113 with L114	30
<input type="checkbox"/>	L114	multi-threading or multithreading or thread\$1	237806
<input type="checkbox"/>	L113	packet process\$3	3592
	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L112	l6 with l38	148
<input type="checkbox"/>	L111	suspend\$3 same L110	3
<input type="checkbox"/>	L110	L6 same L38	194
<input type="checkbox"/>	L109	L6 and L38	752
<input type="checkbox"/>	L108	L100 same L106	3
<input type="checkbox"/>	L107	L100 and L106	105
<input type="checkbox"/>	L106	L38 with suspend\$3	2252
<input type="checkbox"/>	L105	L103 and L104	3
<input type="checkbox"/>	L104	L1 and L101	288
<input type="checkbox"/>	L103	packet\$1 and L102	40
<input type="checkbox"/>	L102	L38.ti,ab,clm. and L100.ti,ab,clm.	129
<input type="checkbox"/>	L101	L38 and L100	4991
<input type="checkbox"/>	L100	router\$1	51745
<input type="checkbox"/>	L99	L89 and L98	6
<input type="checkbox"/>	L98	(712/23).ccls.	694
<input type="checkbox"/>	L97	L65 and L94	1
<input type="checkbox"/>	L96	L49 and L94	1
<input type="checkbox"/>	L95	L89 and L94	4
<input type="checkbox"/>	L94	(712/241).ccls.	240
<input type="checkbox"/>	L93	L70 and L89	0
<input type="checkbox"/>	L92	L70 and L91	0
<input type="checkbox"/>	L91	L38 and L89	1064
<input type="checkbox"/>	L90	L65 and L89	19
<input type="checkbox"/>	L89	packet switch\$3	18844
<input type="checkbox"/>	L88	sun.as. and treat.xp. and L38	8
<input type="checkbox"/>	L87	L38 same L86	33
<input type="checkbox"/>	L86	instruction block\$1	1919

<input type="checkbox"/>	L85	L38 and L83	5
<input type="checkbox"/>	L84	packet\$1 and L83	13
<input type="checkbox"/>	L83	decoupled processor\$1	57
<input type="checkbox"/>	L82	L81 adj processor\$1	5
<input type="checkbox"/>	L81	non-blocking or nonblocking	6578
<input type="checkbox"/>	L80	mon-blocking or nonblocking	1243
<input type="checkbox"/>	L79	L38 with L65	17
<input type="checkbox"/>	L78	L38 adj processor	671
<input type="checkbox"/>	L77	L65 and L70	1
<input type="checkbox"/>	L76	L62 and L70	3
<input type="checkbox"/>	L75	L70 and L74	1
<input type="checkbox"/>	L74	(712/32).ccls.	362
<input type="checkbox"/>	L73	context switch\$3 same L59	30
<input type="checkbox"/>	L72	L59 and L70	1
<input type="checkbox"/>	L71	L49 and L70	0
<input type="checkbox"/>	L70	(712/235).ccls.	175
<input type="checkbox"/>	L69	packet\$1.ti,ab,clm. and L68	8
<input type="checkbox"/>	L68	L65 with (parallel or pipelin\$3)	132
<input type="checkbox"/>	L67	packet\$1.ti,ab,clm. and L65	73
<input type="checkbox"/>	L66	packet.ti,ab,clm. and L65	59
<input type="checkbox"/>	L65	subprocessor\$1 or sub-processor\$1	1077
<input type="checkbox"/>	L64	L38 same L62	29
<input type="checkbox"/>	L63	packet\$1 same L62	17
<input type="checkbox"/>	L62	non-blocking with processor\$1	336
<input type="checkbox"/>	L61	L60 not L58	5
<input type="checkbox"/>	L60	L59 and L53	14
<input type="checkbox"/>	L59	packet with processor\$1	11947
<input type="checkbox"/>	L58	L49 and L53	9
<input type="checkbox"/>	L57	packet\$1 and L52	22
<input type="checkbox"/>	L56	L52 and L55	8
<input type="checkbox"/>	L55	conditional branch\$3	5212
<input type="checkbox"/>	L54	L52 and L53	0
<input type="checkbox"/>	L53	(712/234).ccls.	336
<input type="checkbox"/>	L52	serial processors	98
<input type="checkbox"/>	L51	L38 with L49	26
<input type="checkbox"/>	L50	L38 and L49	167
<input type="checkbox"/>	L49	packet processor\$1	1494

<input type="checkbox"/>	L48	L47 not private branch exchange	35
<input type="checkbox"/>	L47	L45 not L46	46
<input type="checkbox"/>	L46	branch\$3 same L44	5
<input type="checkbox"/>	L45	branch\$3 and L44	51
<input type="checkbox"/>	L44	L38 same L41	236
<input type="checkbox"/>	L43	branch\$3 and L42	471
<input type="checkbox"/>	L42	L38 and L41	2693
<input type="checkbox"/>	L41	decoupled	27354
<input type="checkbox"/>	L40	L38 and L39	5
<input type="checkbox"/>	L39	decoupled architecture	30
<input type="checkbox"/>	L38	multi-threading or multithreading or thread\$1	292691
<input type="checkbox"/>	L37	chained processor\$1	8
<input type="checkbox"/>	L36	L28 and L35	0
<input type="checkbox"/>	L35	L34 with buffer\$1	0
<input type="checkbox"/>	L34	reconfigurable pipeline\$1	89
<input type="checkbox"/>	L33	L29 and L32	2
<input type="checkbox"/>	L32	709/\$.ccls.	36796
<input type="checkbox"/>	L31	router\$1 and L29	17
<input type="checkbox"/>	L30	L28 and L29	2
<input type="checkbox"/>	L29	reservation station\$1	1013
<input type="checkbox"/>	L28	internet protocol or tcp	64244
<input type="checkbox"/>	L27	L25 and L26	18
<input type="checkbox"/>	L26	reservation station\$1 with branch\$3	322
<input type="checkbox"/>	L25	internet protocol or ip or tcp	120655
<input type="checkbox"/>	L24	reservation station\$1 and L23	10
<input type="checkbox"/>	L23	(data or network) adj packet\$1	45419
<input type="checkbox"/>	L22	(router\$1 or switch\$2) and L21	80
<input type="checkbox"/>	L21	packet\$1 and reservation station\$1	149
<input type="checkbox"/>	L20	L18 same next instruction	18
<input type="checkbox"/>	L19	pipeline stage\$1 with conditional branch\$3	105
<input type="checkbox"/>	L18	pipeline stage\$1 with branch\$3	490
<input type="checkbox"/>	L17	L16 not L10	54
<input type="checkbox"/>	L16	stall\$3 and L15	54
<input type="checkbox"/>	L15	L6 same thread\$1	191
<input type="checkbox"/>	L14	L11 and L6	1
<input type="checkbox"/>	L13	serial sub-processors	0
<input type="checkbox"/>	L12	serial subprocessors	0

<input type="checkbox"/>	L11	serial processors	98
<input type="checkbox"/>	L10	L6 and L9	13
<input type="checkbox"/>	L9	stall\$3 same L1	82
<input type="checkbox"/>	L8	stall\$3 and L7	103
<input type="checkbox"/>	L7	L1 and L6	232
<input type="checkbox"/>	L6	packet process\$3	7414
<input type="checkbox"/>	L5	conditional branch\$3 same L4	6
<input type="checkbox"/>	L4	new thread	1557
<input type="checkbox"/>	L3	conditional branch\$3 same L1	20
<input type="checkbox"/>	L2	conditional branch\$3 with L1	2
<input type="checkbox"/>	L1	context adj (switch\$3 or swap\$4)	3938

END OF SEARCH HISTORY